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KIRK NICHOLSON

A Practical Approach Rockport Pub

Beginning with discussions on the operation of electronic devices and analysis of the nucleus of digital design, the text addresses: the impact of interconnect, design for low power, issues in timing and clocking, design methodologies, and the effect of design automation on the digital design perspective.

Basic VLSI Design Prentice Hall

Aimed primarily for undergraduate students pursuing courses in VLSI design, the book emphasizes the physical understanding of underlying principles of the subject. It not only focuses on circuit design process obeying VLSI rules but also on technological aspects of Fabrication. VHDL modeling is discussed as the design engineer is expected to have good knowledge of it. Various Modeling issues of VLSI devices are focused which includes necessary device physics to the required level. With such an in-depth coverage and practical approach practising engineers can also use this as ready reference.

Design for Testability Elsevier

This book provides step-by-step guidance on how to design VLSI systems using Verilog. It shows the way to design systems that are device, vendor and technology independent. Coverage presents new material and theory as well as synthesis of recent work with complete Project Designs using industry standard CAD tools and FPGA boards. The reader is taken step by step through different designs, from implementing a single digital gate to a massive design consuming well over 100,000 gates. All the design codes developed in this book are Register Transfer Level (RTL) compliant and can be readily used or amended to suit new projects.

Modern VLSI Design John Wiley & Sons

The fourth edition of CMOS Digital Integrated Circuits: Analysis and Design continues the well-established tradition of the earlier editions by offering the most comprehensive coverage of digital CMOS circuit design, as well as addressing state-of-the-art technology issues highlighted by the widespread use of nanometer-scale CMOS technologies. In this latest edition, virtually all chapters have been re-written, the transistor model equations and device parameters have been revised to reflect the significant changes that must be taken into account for new technology generations, and the material has been reinforced with up-to-date examples. The broad-ranging coverage of this textbook starts with the fundamentals of CMOS process technology, and continues with MOS transistor models, basic CMOS gates, interconnect effects, dynamic circuits, memory circuits, arithmetic building blocks, clock and I/O circuits, low

power design techniques, design for manufacturability and design for testability.

Introduction to VLSI Design Prentice Hall

Very Large Scale Integration (VLSI) has become a necessity rather than a specialization for electrical and computer engineers. This unique text provides Engineering and Computer Science students with a comprehensive study of the subject, covering VLSI from basic design techniques to working principles of physical design automation tools to leading edge application-specific array processors. Beginning with CMOS design, the author describes VLSI design from the viewpoint of a digital circuit engineer. He develops physical pictures for CMOS circuits and demonstrates the top-down design methodology using two design projects - a microprocessor and a field programmable gate array. The author then discusses VLSI testing and dedicates an entire chapter to the working principles, strengths, and weaknesses of ubiquitous physical design tools. Finally, he unveils the frontiers of VLSI. He emphasizes its use as a tool to develop innovative algorithms and architecture to solve previously intractable problems. VLSI Design answers not only the question of "what is VLSI," but also shows how to use VLSI. It provides graduate and upper level undergraduate students with a complete and congregated view of VLSI engineering.

VLSI Design Springer Science & Business Media

The access network is expected to be one of the major battlegrounds of telecommunications network operators, since upgrades of the existing narrowband access network will be the critical factor in supplying multimedia broadband services in a competitive market. The future broadband access network architecture needs to be flexible enough to efficiently support the provision of a full set of broadband and narrowband services with a wide range of capacity demands. A wide range of broadband access technologies are available. Furthermore, the key issues in the upgrading of the very cost sensitive access network are financial as well as technological, both for incumbent and new entrant operators. Thus, in order to identify minimum-risk introductory strategies the economic viability of access network broadband upgrades needs to be carefully assessed. However, despite the definite need for techno-economic evaluations, very few books have been published in this field. One of the reasons might be that broadband access network upgrading only very recently gained wide recognition as a key challenge for broadband delivery. Secondly, this kind of strategic work and these studies tend to be considered rather sensitive by operators, and thus both results and methodologies are not usually readily available. Thirdly, the work reported in this book in many respects was a major pioneering effort, which quite ambitiously aimed at modelling the whole life-cycle costs and revenue streams of access network upgrades, as opposed to several other efforts,

which often are limited to pure investment cost comparisons.

A Design Perspective McGraw-Hill Companies

CD-ROM contains: AIM SPICE (from AIM Software) -- Micro-Cap 6 (from Spectrum Software) -- Silos III Verilog Simulator (from Simucad) -- Adobe Acrobat Reader 4.0 (from Adobe).

CMOS VLSI Design: A Circuits and Systems Perspective I. K. International Pvt Ltd

iming, timing, timing! That is the main concern of a digital designer charged with designing a semiconductor chip. What is it, how is it T described, and how does one verify it? The design team of a large digital design may spend months architecting and iterating the design to achieve the required timing target. Besides functional verification, the t- ing closure is the major milestone which dictates when a chip can be - leased to the semiconductor foundry for fabrication. This book addresses the timing verification using static timing analysis for nanometer designs. The book has originated from many years of our working in the area of timing verification for complex nanometer designs. We have come across many design engineers trying to learn the background and various aspects of static timing analysis. Unfortunately, there is no book currently ava- able that can be used by a working engineer to get acquainted with the - tails of static timing analysis. The chip designers lack a central reference for information on timing, that covers the basics to the advanced timing veri- cation procedures and techniques.

Digital Logic and Computer Design Pearson Education India

This unique, go-to guide for designers fully details the essential layout and design skills needed to succeed in this competitive industry. With fun and practical application, it offers valuable insight into strategy and business when working in the real world with real clients, starting with basic information on layout principles before delving more deeply into theory and application on a project-by-project basis. Illustrated with real-world assignments and case studies, this guide offers a behind-the-scenes take on the entire process and steps necessary to go from concept to final outcome, including how to overcome challenges presented along the way.

Fundamentals of Digital Logic Design McGraw-Hill College

As advances in technology and circuit design boost operating frequencies of microprocessors, DSPs and other fast chips, new design challenges continue to emerge. One of the major performance limitations in today's chip designs is clock skew, the uncertainty in arrival times between a pair of clocks. Increasing clock frequencies are forcing many engineers to rethink their timing budgets and to use skew-tolerant circuit techniques for both domino and static circuits. While senior designers have long developed their own techniques for reducing the sequencing overhead of domino circuits, this knowledge has routinely been protected as trade secret and has rarely been shared. Skew-

Tolerant Circuit Design presents a systematic way of achieving the same goal and puts it in the hands of all designers. This book clearly presents skew-tolerant techniques and shows how they address the challenges of clocking, latching, and clock skew. It provides the practicing circuit designer with a clearly detailed tutorial and an insightful summary of the most recent literature on these critical clock skew issues. * Synthesizes the most recent advances in skew-tolerant design in one cohesive tutorial * Provides incisive instruction and advice punctuated by humorous illustrations * Includes exercises to test understanding of key concepts and solutions to selected exercises

A Systems Perspective with Verilog/VHDL Manual Springer Science & Business Media

The award-winning VLSI design guide is now fully updated to reflect the latest advances in chip design

Principles of CMOS VLSI Design Springer Science & Business Media

For Electrical Engineering and Computer Engineering courses that cover the design and technology of very large scale integrated (VLSI) circuits and systems. May also be used as a VLSI reference for professional VLSI design engineers, VLSI design managers, and VLSI CAD engineers. Modern VLSI Design provides a comprehensive "bottom-up" guide to the design of VLSI systems, from the physical design of circuits through system architecture with focus on the latest solution for system-on-chip (SOC) design. Because VLSI system designers face a variety of challenges that include high performance, interconnect delays, low power, low cost, and fast design turnaround time, successful designers must understand the entire design process. The Third Edition also provides a much more thorough discussion of hardware description languages, with introduction to both Verilog and VHDL. For that reason, this book presents the entire VLSI design process in a single volume.

Principles and Applications Morgan Kaufmann

Top-down approach to practical, tool-independent, digital circuit design, reflecting how circuits are designed.

Skew-tolerant Circuit Design North Holland

The Complete, Modern Tutorial on Practical VLSI Chip Design, Validation, and Analysis As microelectronics engineers design complex chips using existing circuit libraries, they must ensure correct logical, physical, and electrical properties, and prepare for reliable foundry fabrication. VLSI Design Methodology Development focuses on the design and analysis steps needed to perform these tasks and successfully complete a modern chip design. Microprocessor design authority Tom Dillinger carefully introduces core concepts, and then guides engineers through modeling, functional design validation, design implementation, electrical analysis, and release to manufacturing. Writing from the engineer's perspective, he covers underlying EDA tool algorithms, flows, criteria for assessing project status, and key tradeoffs and interdependencies. This fresh and accessible tutorial will be valuable to all VLSI system designers, senior undergraduate or graduate students of microelectronics design, and companies offering internal courses for engineers at all levels. Reflect complexity, cost, resources, and schedules in planning a chip

design project Perform hierarchical design decomposition, floorplanning, and physical integration, addressing DFT, DFM, and DFY requirements Model functionality and behavior, validate designs, and verify formal equivalency Apply EDA tools for logic synthesis, placement, and routing Analyze timing, noise, power, and electrical issues Prepare for manufacturing release and bring-up, from mastering ECOs to qualification This guide is for all VLSI system designers, senior undergraduate or graduate students of microelectronics design, and companies offering internal courses for engineers at all levels. It is applicable to engineering teams undertaking new projects and migrating existing designs to new technologies.

Circuits and Systems Springer Science & Business Media

At the beginning we would like to introduce a refinement. The term 'VLSI planarization' means planarization of a circuit of VLSI, i.e. the embedding of a VLSI circuit in the plane by different criteria such as the minimum number of connectors, the minimum total length of connectors, the minimum number of over-the-element routes, etc. A connector is designed to connect the broken sections of a net. It can be implemented in different ways depending on the technology. Connectors for a bipolar VLSI are implemented by diffused tunnels, for instance. By over-the-element route we shall mean a connection which intersects the enclosing rectangle of an element (or a cell). The possibility of the construction such connections during circuit planarization is reflected in element models and can be ensured, for example, by the availability of areas within the rectangles where connections may be routed. VLSI planarization is one of the basic stages (others will be discussed below) of the so called topological (in the mathematical sense) approach to VLSI design. This approach does not lie in the direction of the classical approach to automation of VLSI layout design. In the classical approach to computer aided design the placement and routing problems are solved successively. The topological approach, in contrast, allows one to solve both problems at the same time. This is achieved by constructing a planar embedding of a circuit and obtaining the proper VLSI layout on the basis of it.

Multiprocessor Computer Architectures McGraw-Hill College

Earthquake Resistant Design and Risk Reduction, 2nd edition is based upon global research and development work over the last 50 years or more, and follows the author's series of three books Earthquake Resistant Design, 1st and 2nd editions (1977 and 1987), and Earthquake Risk Reduction (2003). Many advances have been made since the 2003 edition of Earthquake Risk Reduction, and there is every sign that this rate of progress will continue apace in the years to come. Compiled from the author's wide design and research experience in earthquake engineering and engineering seismology, this key text provides an excellent treatment of the complex multidisciplinary process of earthquake resistant design and risk reduction. New topics include the creation of low-damage structures and the spatial distribution of ground shaking near large fault ruptures. Sections on guidance for developing countries, response of buildings to differential settlement in liquefaction, performance-based and displacement-based design and the architectural aspects of earthquake resistant design are heavily revised. This book: Outlines individual

national weaknesses that contribute to earthquake risk to people and property Calculates the seismic response of soils and structures, using the structural continuum "Subsoil - Substructure - Superstructure - Non-structure" Evaluates the effectiveness of given design and construction procedures for reducing casualties and financial losses Provides guidance on the key issue of choice of structural form Presents earthquake resistant design methods for the main four structural materials - steel, concrete, reinforced masonry and timber - as well as for services equipment, plant and non-structural architectural components Contains a chapter devoted to problems involved in improving (retrofitting) the existing built environment This book is an invaluable reference and guiding tool to practising civil and structural engineers and architects, researchers and postgraduate students in earthquake engineering and engineering seismology, local governments and risk management officials.

System-on-Chip Design PHI Learning Pvt. Ltd.

Hardware -- Logic Design.

Digital VLSI Systems Design John Wiley & Sons Incorporated

Low-Power Digital VLSI Design: Circuits and Systems addresses both process technologies and device modeling. Power dissipation in CMOS circuits, several practical circuit examples, and low-power techniques are discussed. Low-voltage issues for digital CMOS and BiCMOS circuits are emphasized. The book also provides an extensive study of advanced CMOS subsystem design. A low-power design methodology is presented with various power minimization techniques at the circuit, logic, architecture and algorithm levels. Features: Low-voltage CMOS device modeling, technology files, design rules Switching activity concept, low-power guidelines to engineering practice Pass-transistor logic families Power dissipation of I/O circuits Multi- and low-VT CMOS logic, static power reduction circuit techniques State of the art design of low-voltage BiCMOS and CMOS circuits Low-power techniques in CMOS SRAMS and DRAMS Low-power on-chip voltage down converter design Numerous advanced CMOS subsystems (e.g. adders, multipliers, data path, memories, regular structures, phase-locked loops) with several design options trading power, delay and area Low-power design methodology, power estimation techniques Power reduction techniques at the logic, architecture and algorithm levels More than 190 circuits explained at the transistor level.

Practical Low Power Digital VLSI Design Basic Vlsi Design 3Rd Ed. Basic VLSI Design Basic VLSI Design Principles and Applications

This solutions manual is for undergraduate VLSI design courses. Its emphasis is on the relationship between circuit layout design and electrical system performance, and it covers topics such as the basic physics of devices and introductory VLSI computer systems in CMOS and NMOS.

Systems and Circuits Addison-Wesley

Basic Vlsi Design 3Rd Ed. Basic VLSI Design Basic VLSI Design Principles and Applications Prentice Hall Essentials Of Vlsi Circuits And Systems PHI Learning Pvt. Ltd. Basic VLSI Design Principles and Applications Prentice Hall VLSI Design. K. International Pvt Ltd

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